PATENT ABSTRACTS OF JAPAN

(11) Publication number:

11-331175

(43) Date of publication of application: 30.11.1999

(51)Int.CI.

H04L 12/28 H04B 1/40 H040 7/38

H04L 12/56

(21)Application number: 10-127480

(71)Applicant : SONY CORP

(22)Date of filing:

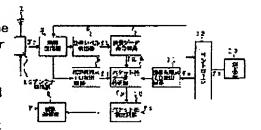
11.05.1998

(72)Inventor: ANDO YOSHINORI

(54) RADIO COMMUNICATION SYSTEM

(57) Abstract:

PROBLEM TO BE SOLVED: To enable satisfactory transmission/reception at all times while preventing the reduction of communication ability corresponding to the change of a transmission/reception environment. SOLUTION: The ID data of a counter terminal are detected from a received signal by a counter side terminal ID extracting circuit 6, the reception level of the received signal is detected by a reception level detector 5, control data for correcting reference packet length data are outputted from a control data output circuit 7 based on that reception level, packet length data stored in a packet length data memory 8 are corrected based on the control data, the packet length data are read out of the packet length data memory 8 by a packet length setting circuit 10 at the time of transmission, the transmission is performed based on the corrected packet length data corresponding to the ID of the counter side terminal, and data can always be transmitted in a state of high communication capability,



while reducing the disappearance of packets due to errors with the optimum packet length which corresponds to the state of received radio waves.